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Sheet	2	of	2
		Attorney Docket Number	
		YOR920030249US1	
Application Number 10/644210 Filing Date 8-20-2013 First Named Inventor Rivers Art Unit 2189 Examiner Name R. Elmore			

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.) date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
RKF		M ZHANG AND K. ASANOVICH, Highly Associative Caches for Low-Power Processors, Kool Chips Workshop, 33rd Int'l Symposium on Microarchitecture, Published in: US	
RIE		JUCE A. RIVERS, Performance of High-Bandwidth Multi-Lateral Cache Organizations, 4/1/1998, Page(s) 143-147	

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